

Monolithic Quantum dot Laser for Silicon Photonics

Siming Chen

Royal Academy of Engineering Fellow / Lecturer

University College London, UK

siming.chen@ucl.ac.uk

OPTICS

14 April 2021

Outline

- I. Why monolithic III-V lasers on Si and challenges
- II. Quantum dots for monolithic III-V/Si integration
- III. Monolithic 1.3 μm InAs quantum dot lasers on Si
- IV. Conclusion

Outline

- I. Why Monolithic III-V lasers on Si and challenges**
- II. Quantum dots for monolithic III-V/Si integration
- III. Monolithic 1.3 μm InAs quantum dot lasers on Si
- IV. Conclusion

Challenges for today's networks



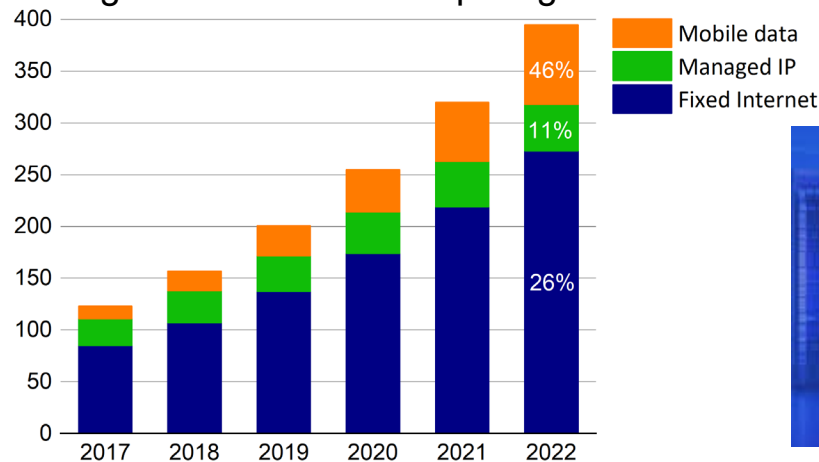
Optical Communications



High-Performance Computing



5 G



Source: Cisco Global IP Traffic Forecast, 2017-2022



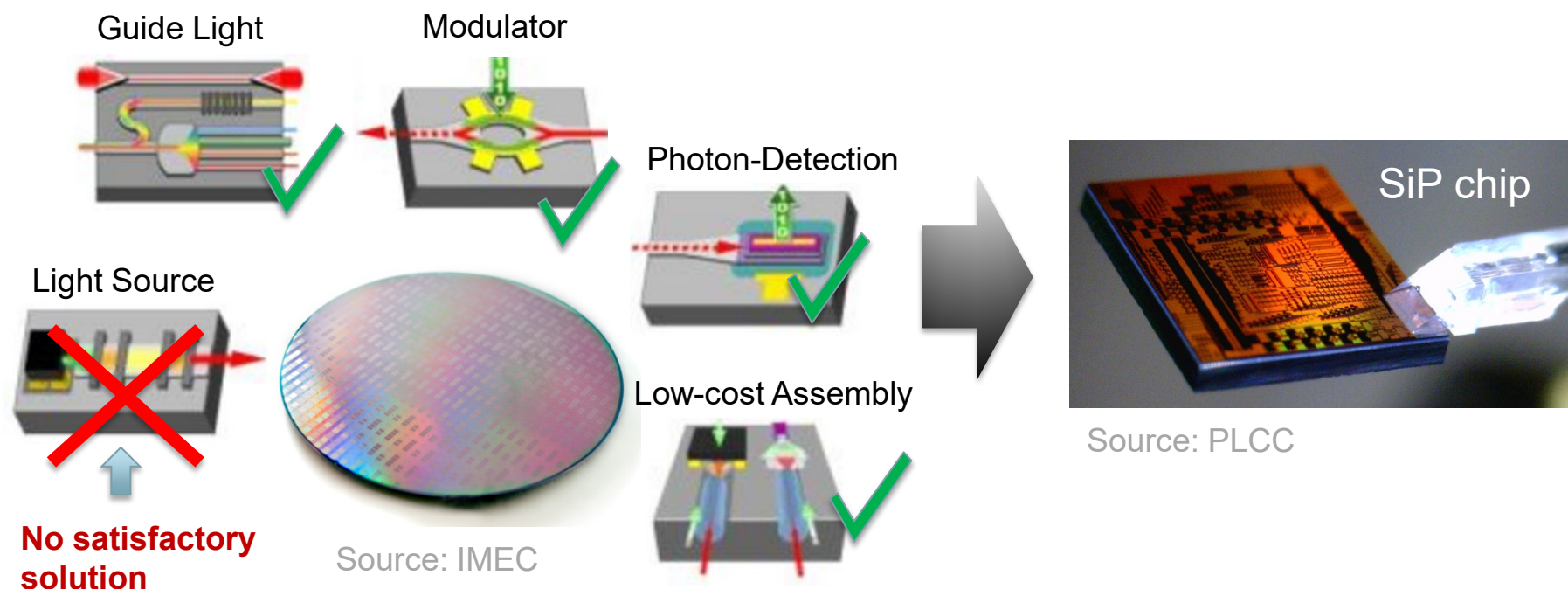
Video downloading



Cloud Storage

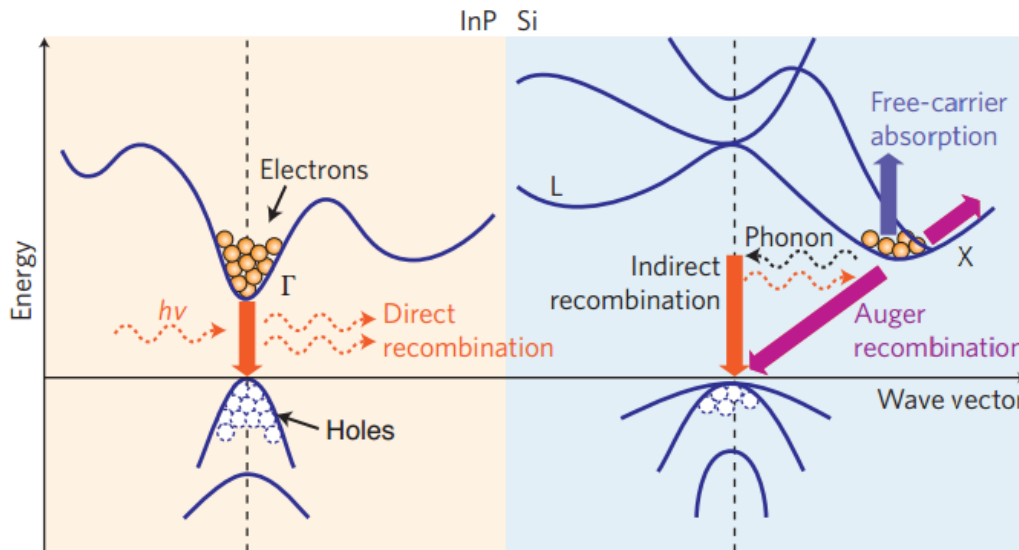
- Global IP traffic will increase threefold over the next 5 years
- “Bottleneck” is represented with existing technologies

Why silicon photonics



- A innovating technology: convergence of photonics and electronics using **standard CMOS fab**.
- The vision: to provide optical connectivity everywhere, ranging from network level to chip-to-chip and to on-chip.
- Many individual building blocks have been realized and embedded at the **SOI wafer level**, except for the **light source**.

Challenges for silicon photonics



Origin of poor emitter?

- Indirect bandgap
- Extremely **poor** internal quantum efficiency η_i , which is defined as:

$$\eta_i = \frac{\tau_{nonrad}}{\tau_{nonrad} + \tau_{rad}}$$

- and is generally of the order of 10^{-6} .

Two major non-radiative processes:

- Auger recombination
- Free-carrier absorption

Combine III-Vs and Si

Si

- Cost advantage
- Light routing/modulating/ detecting
- Transparent at the infrared wavelengths
- High index contrast
- **Poor emitter** 😞

III-Vs

- Light emitting
- Dominate for near- and mid-infrared wavelengths
- **Expensive** 😞
- **Small size** 😞



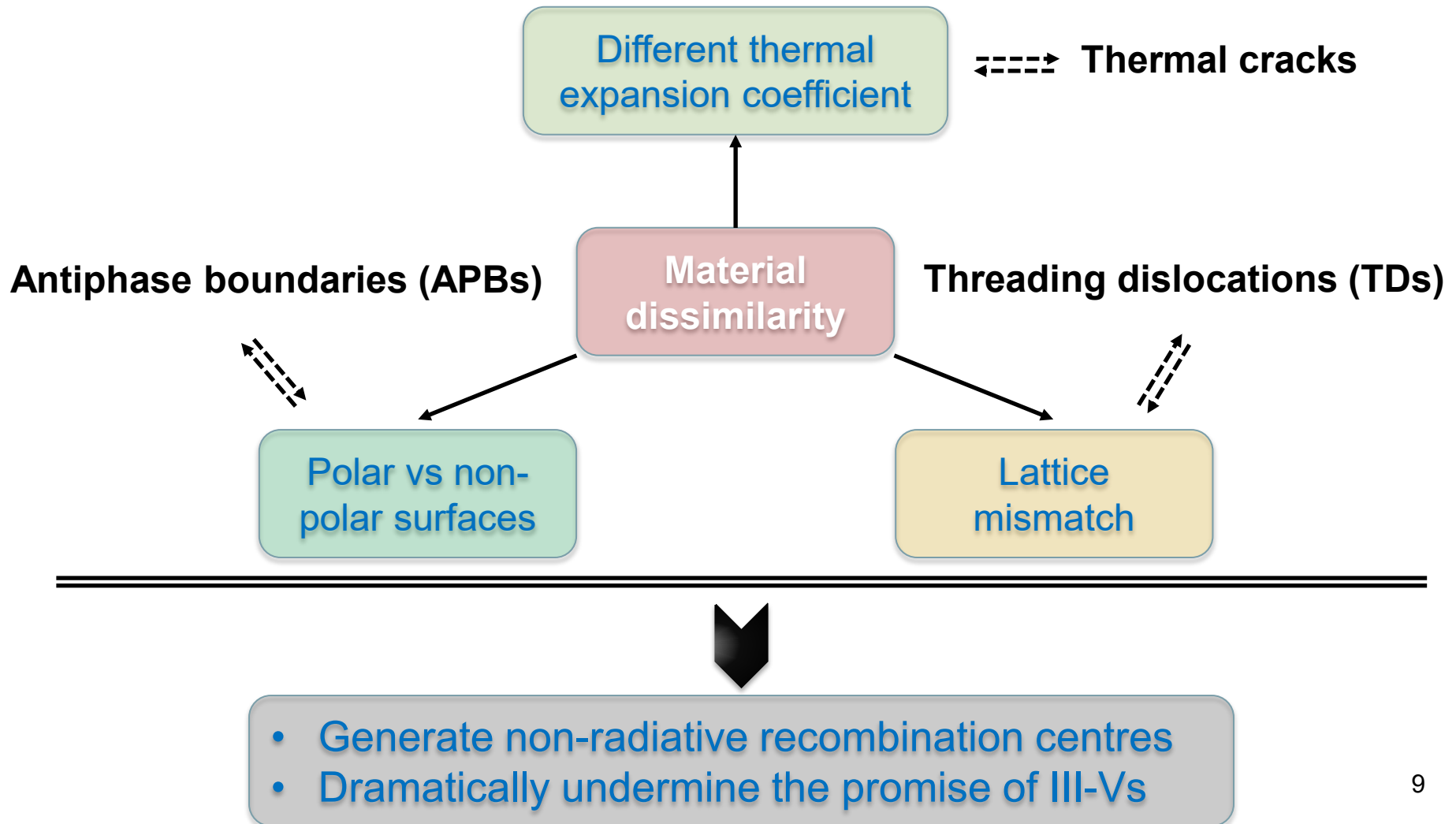
Direct epitaxy

Comparison between different heterogeneous integration strategies on silicon

	Integration density	CMOS compatibility	Cost	Overall maturity
Heterogeneous bonding	Medium	Potentially back-end compatible	Medium	Mature
Transfer printing	High	Potentially back-end compatible	Low	R&D
Epitaxial growth	Very High	Potentially front-end compatible	Potentially very low	R&D
Colloidal QDs	High	Potentially back-end compatible	Potentially very low	Early R&D

- **Wafer bonding**
 - most mature technology
- **Epitaxial growth**
 - cost-effective, massive scalable and streamlined fabrication.

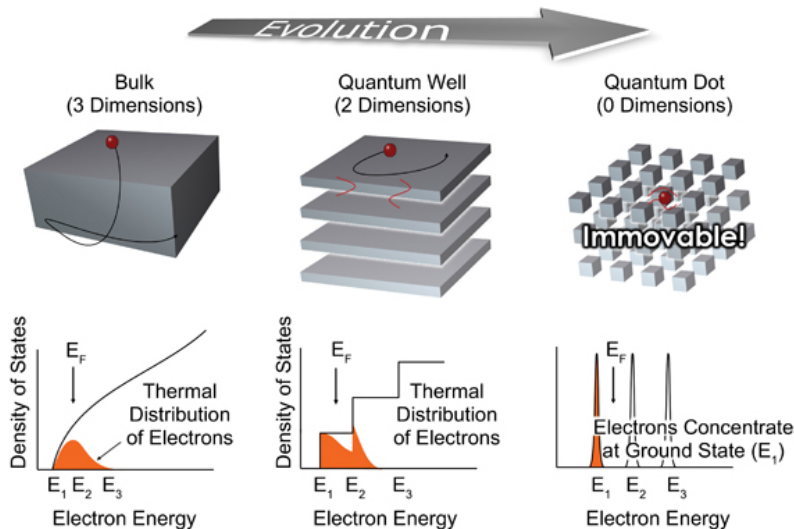
Challenges for epitaxial growth



Outline

- I. Why monolithic III-V lasers on Si and challenges
- II. Quantum dots for monolithic III-V/Si integration**
- III. Monolithic 1.3 μm InAs quantum dot lasers on Si
- IV. Conclusion

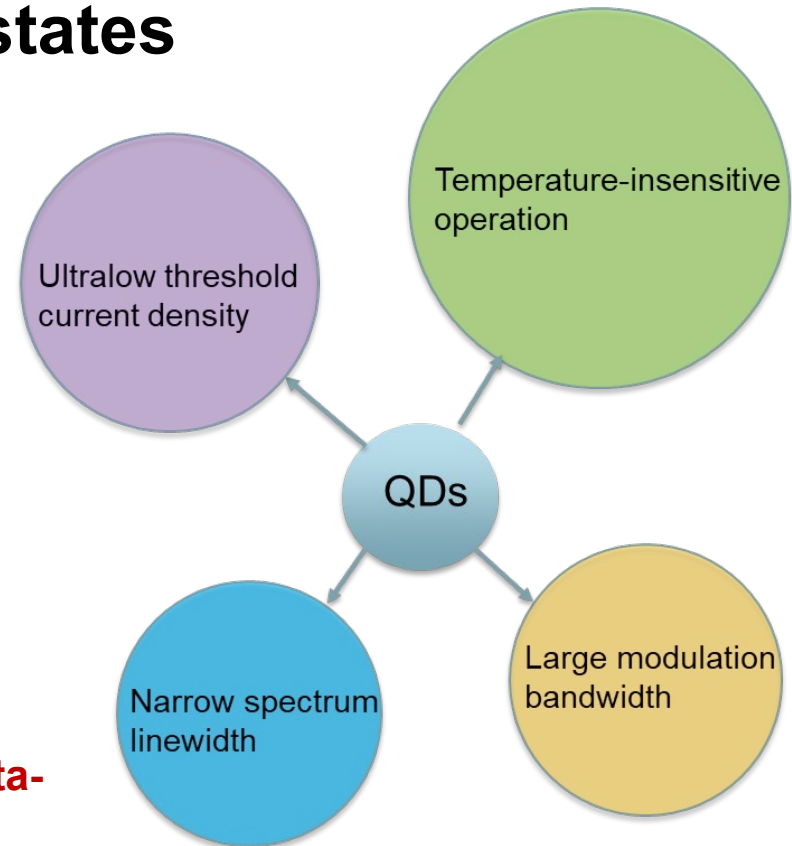
Delta-function density of states



Evolution of density of states

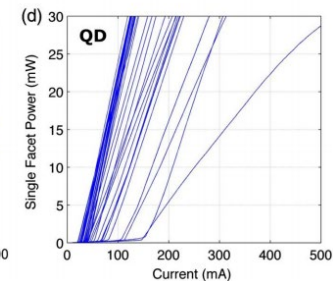
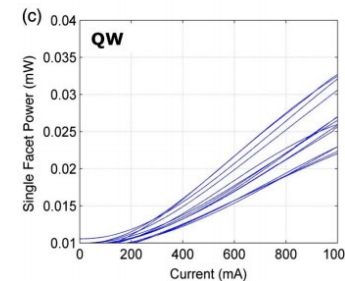
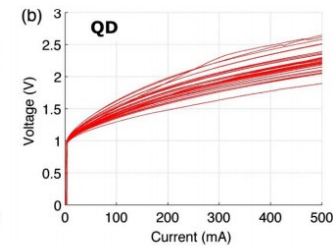
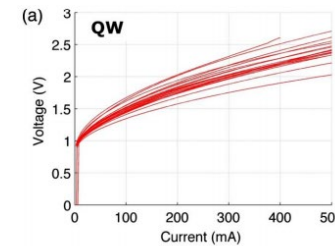
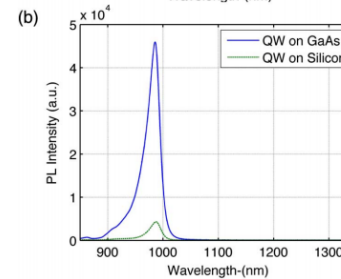
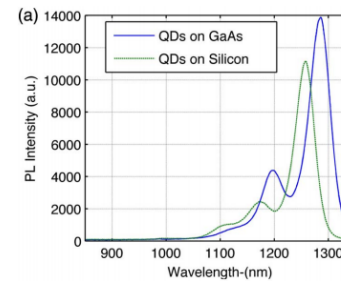
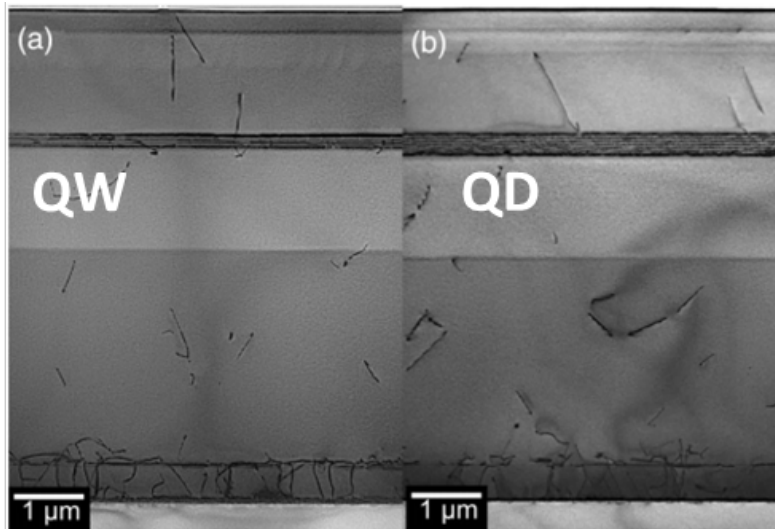
For a QD ensemble, DOS represents a set of **delta-function peaks** centred at the atomic-like energy levels.

- The density of charge carriers accumulated at the energy of the working transition is significantly enhanced – **Higher differential gain & reduced threshold current density**.
- A large energy separation between ground and excited states – **temperature independent operation**.



Conventional advantages of QDs

Unique advantage of QDs - less sensitive to defects



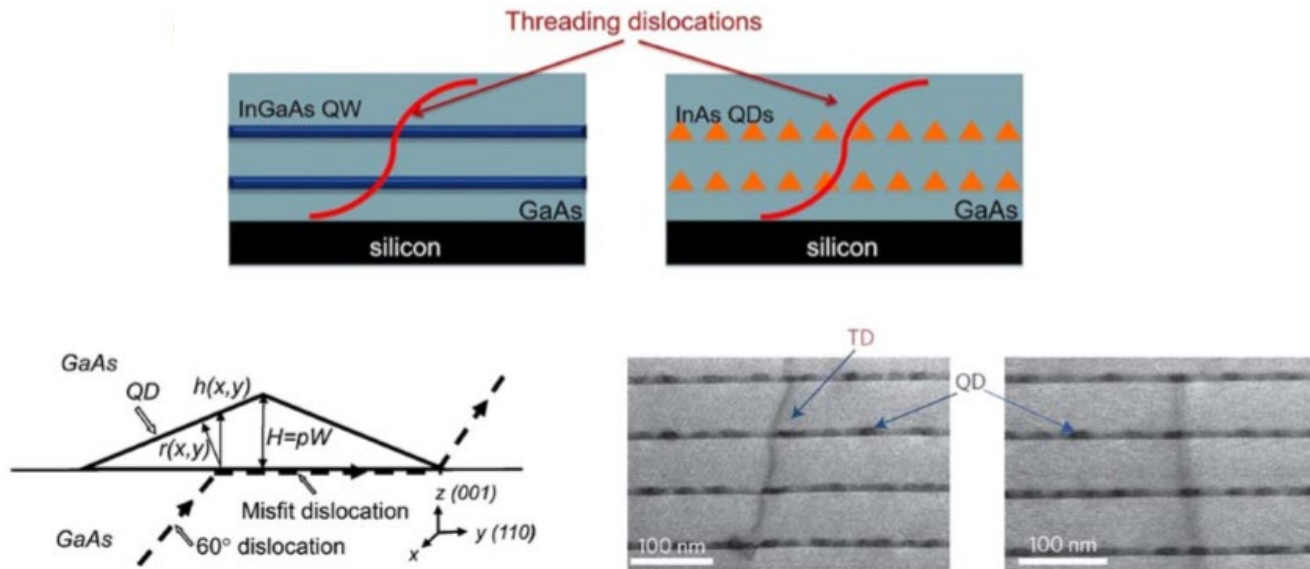
- Direct comparison of QD versus QW lasers grown on Si
- Similar defect density; processing, and measurement techniques were identical

Similar IV characteristics

Totally different optical properties and laser performance

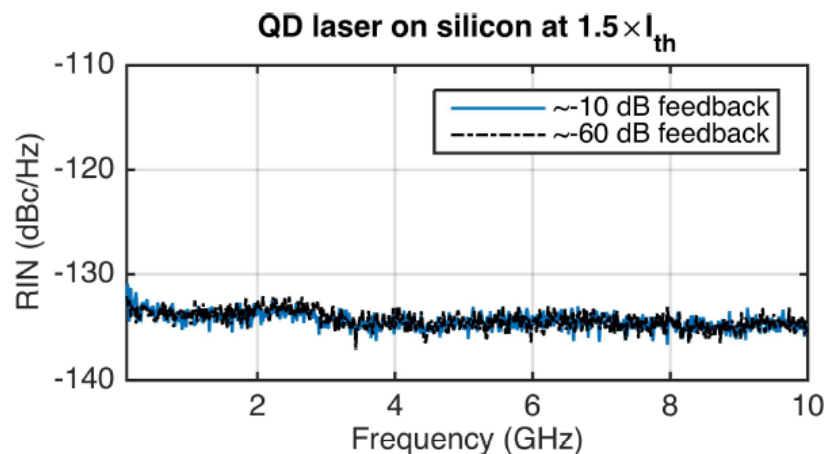
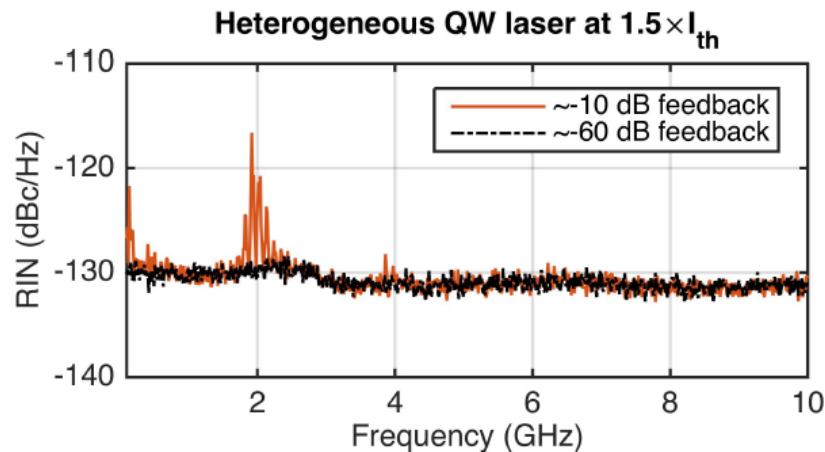
Less sensitive to defects for QDs
Why?

Origin of reduced sensitivity to defects



- Carriers are highly localized in QD and hence reduce the probability of interaction with defects
- One TD can only one or a very limited number of dots, while still leaving the rest of dots providing sufficient optical gain – **the number of dots $\gg$ the number of TDs**
- The dislocation bending can occur beneath the QDs when strain energy is released through the generation of the misfit dislocation and a TD propagates toward the bottom of a QD. Consequently, the bending of dislocation generated a segment of misfit dislocation gliding below the island.
- The TD can also be effectively propelled away from QDs due to the strong strain field introduced by the QDs.

Unique advantage of QDs - less sensitive to feedback



The critical feedback level where the laser enters a coherence collapse regime is given by:

$$f_{crit} = \frac{\tau_L^2 (K f_r^2 + \gamma_0)^2}{16 |C_e|^2} \left(\frac{1 + \alpha^2}{\alpha^4} \right)$$

where

K: K-factor

α : linewidth enhancement factor

- highly damped relaxation oscillations
- lower amplitude-phase coupling

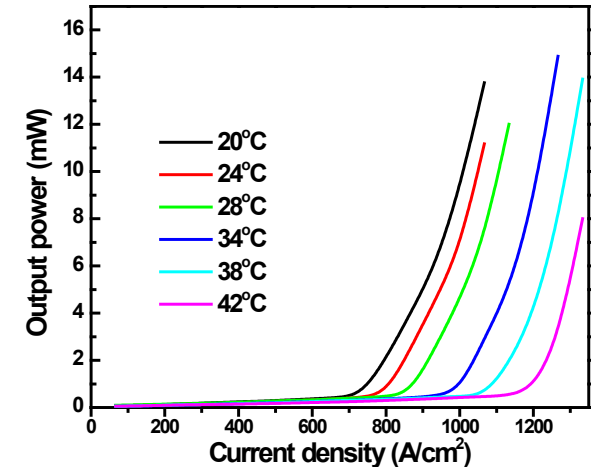
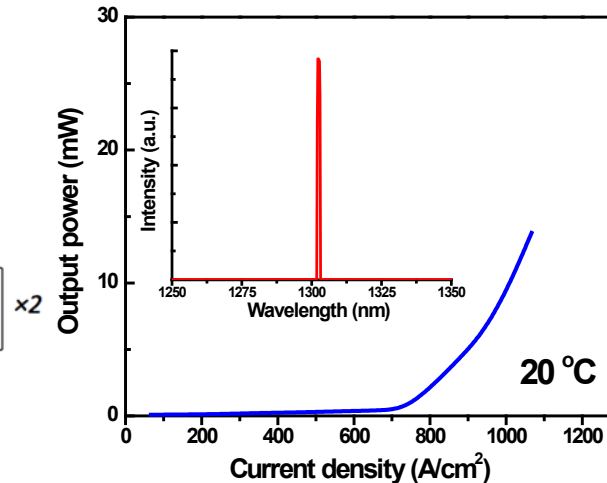
Outline

- I. Why monolithic III-V lasers on Si and challenges
- II. Quantum dots for monolithic III-V/Si integration
- III. Monolithic 1.3 μm InAs quantum dot lasers on Si**
- IV. Conclusion

First 1.3 μm III-V QD laser grown on Si

2011

50nm GaAs
100nm $\text{Al}_{0.4}\text{Ga}_{0.6}\text{As}$
50nm GaAs
5 layer InAs/InGaAs DWELL
50nm GaAs
100 layer GaAs/AlGaAs SPLs
400nm GaAs
5 layer $\text{In}_{0.15}\text{Ga}_{0.85}\text{As/GaAs SPLs}$
1 μm GaAs
Si Substrate

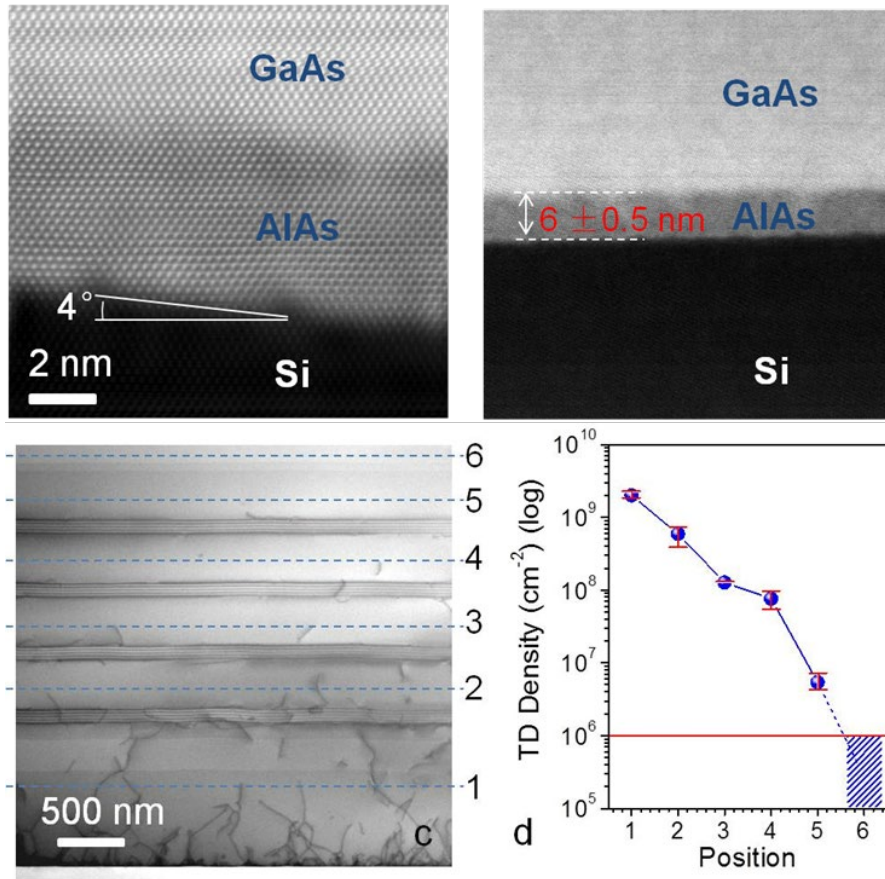


- To present the formation of APBs
 - Offcut Si (100) wafers
- To suppress the propagation of TDs
 - Two sets of InGaAs/GaAs DFLs

Not good enough

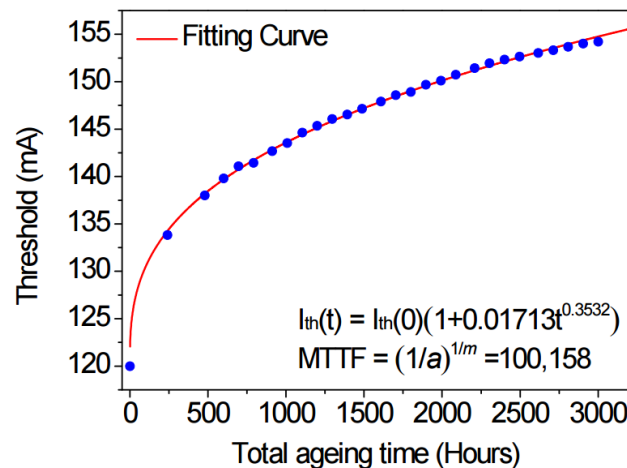
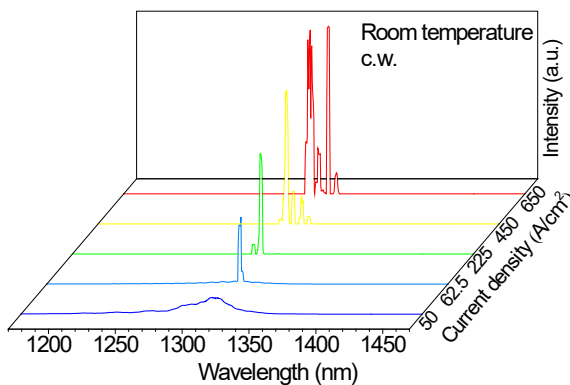
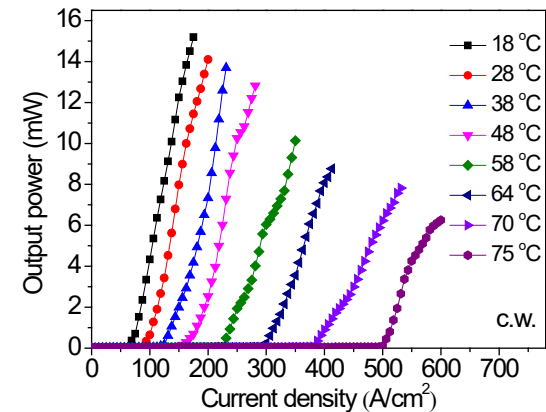
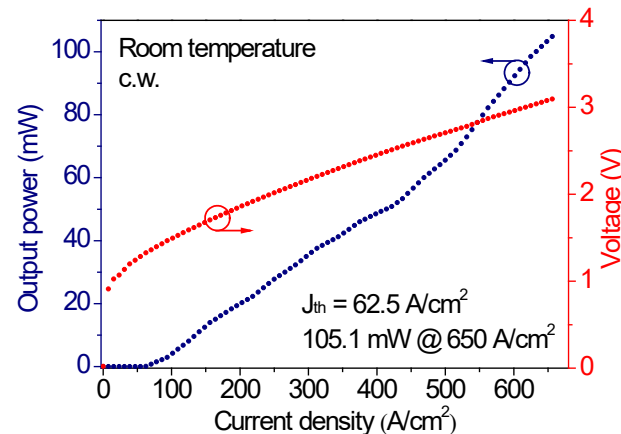
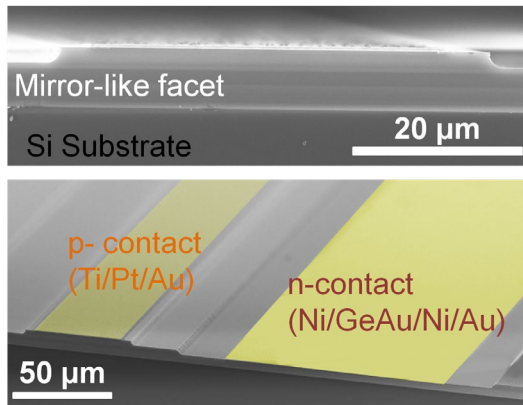
- Off-cut substrate
- Pulsed operation
- $J_{\text{th}} = 725 \text{ A}/\text{cm}^2$ (RT);
- $P_{\text{out}} = 26 \text{ mW}/\text{facet}$ at (RT)
- $\lambda_{\text{peak}} = 1300 \text{ nm}$
- $T_{\text{max}} = 42 \text{ }^\circ\text{C}$

First long lifetime III-V QD laser grown on Si **2016**



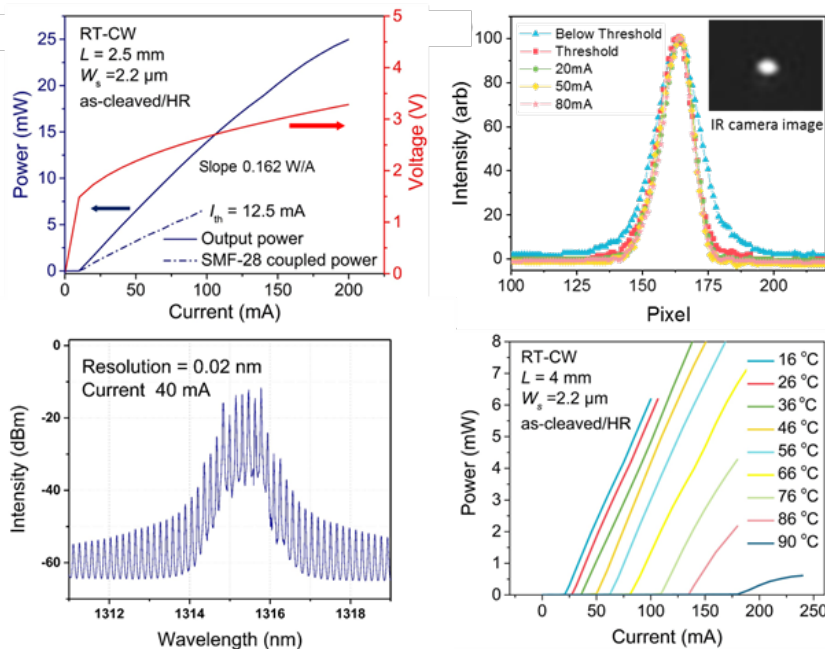
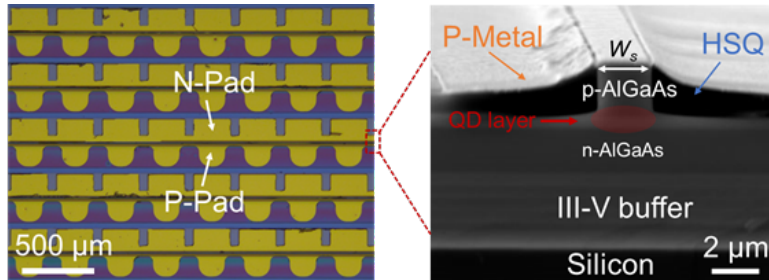
- Si (100) wafers with 4° **offcut** to the [011] plane
 - to prevent the formation of APBs
- **AlAs nucleation layer**
 - to suppress three-dimensional growth
- **InGaAs/GaAs DFLs**
 - to suppress the propagation of the TDs
- **In-situ thermal annealing** of SLS
 - to improve the efficacy of filtering defects.

First long lifetime III-V QD laser grown on Si

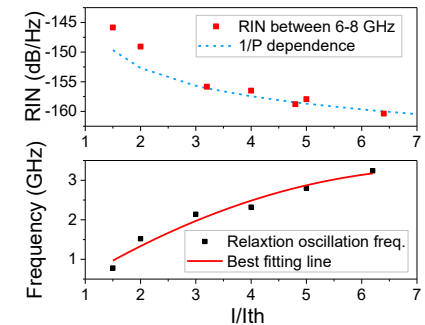
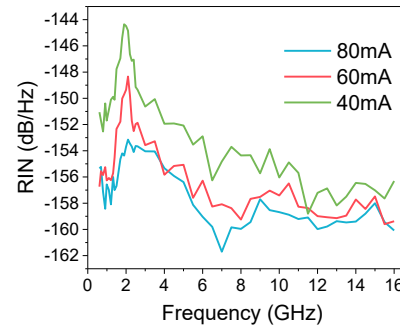
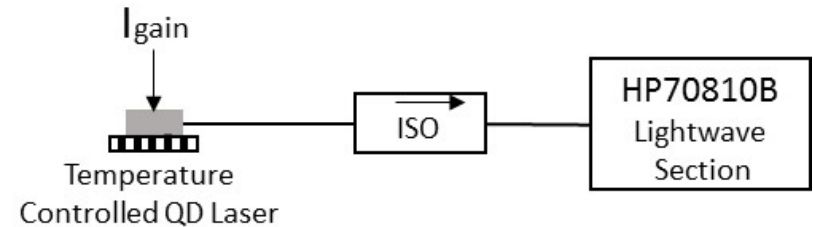


- Broad area laser
- $J_{\text{th}} = 62.5 \text{ A}/\text{cm}^2$ (RT)
- $P_{\text{out}} = 105 \text{ mW}/\text{facet}$ at (RT)
- $\lambda_{\text{peak}} = 1315 \text{ nm}$
- $T_{\text{max}} = 75 \text{ °C}$ (c.w.) **120 °C** (pulsed)
- Extrapolated lifetime over **100,000** hours

Ridge waveguide laser

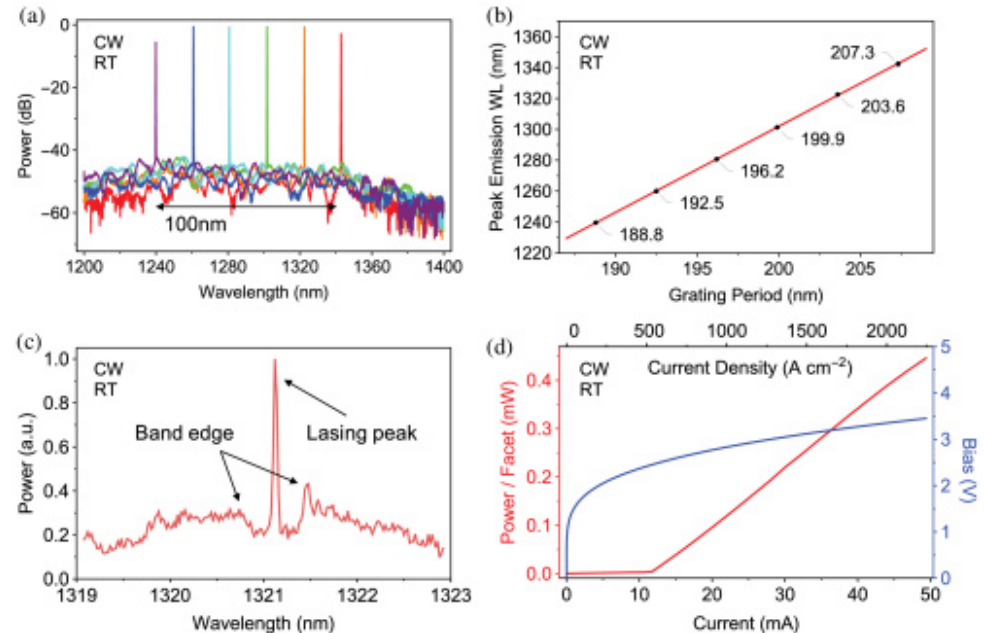
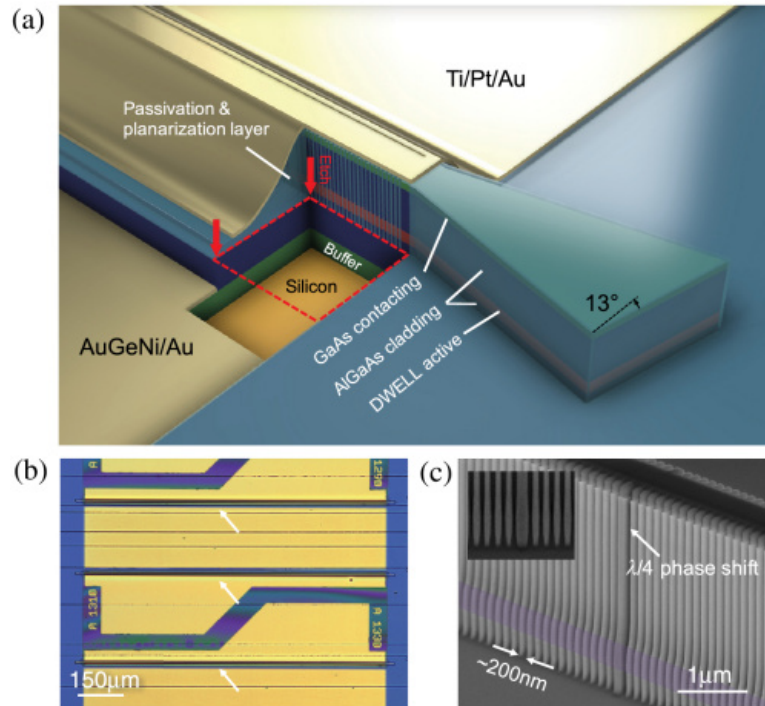


M. Liao et al., Photon. Res. **6**, 1062 (2018)



- Offcut substrate
- $I_{th} \sim 12.5 \text{ mA @RT (CW)}$
- $T_{max} \sim 90 \text{ }^{\circ}\text{C (CW)}$
- $\lambda_{peak} \sim 1315 \text{ nm (ideal for O-band)}$
- $RIN < -150 \text{ dB/Hz.}$
- The relaxation oscillation frequency (ω_R) from 1 GHz to 3 GHz (20 mA - 80 mA)
- Low ω_R is a result of longer photon lifetime due to its longer cavity length (2.5 mm).

The first QD DFB laser array on Si

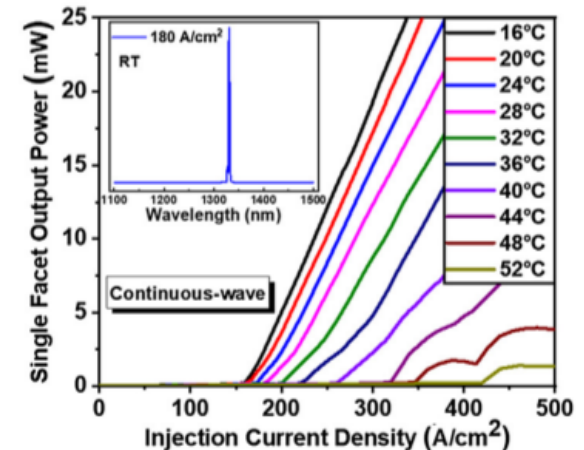
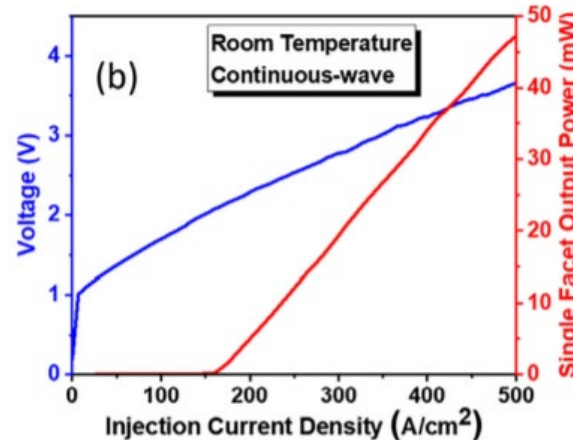
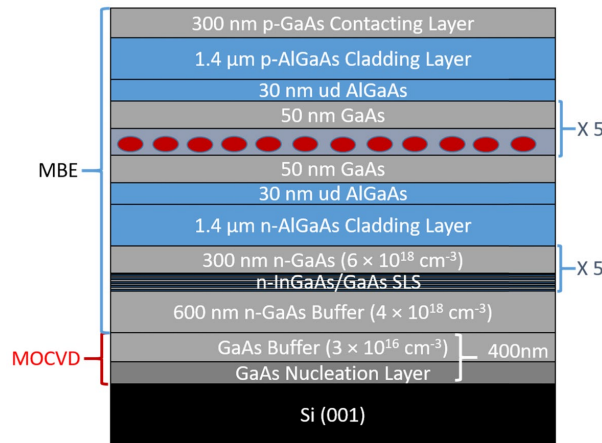


- Lateral surface gratings design (first order)
- $\lambda/4$ phase shift
- Different grating periods for λ turnability
- AR-coupler design

- Offcut substrate
- $I_{th} \sim 12\text{mA}$
- SMSR $\sim 50\text{ dB}$
- Wavelength coverage $\sim 100\text{nm}$
- CWDM compatible (20nm spacing)

InAs QD laser grown on on-axis Si (001)

2017



Key Features

- Leverage Leti's APB-free GaAs/Si virtual substrate technique
- Combine MOCVD and MBE
- Without any intermediate buffer layer or patterned substrate

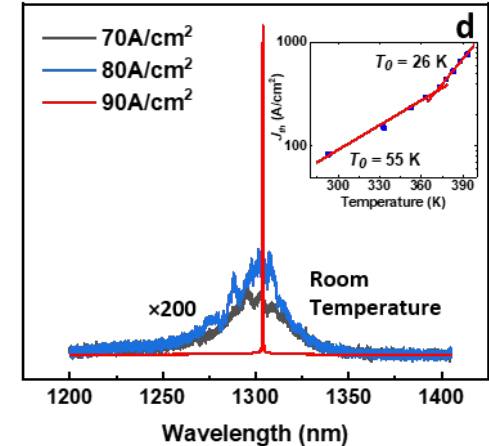
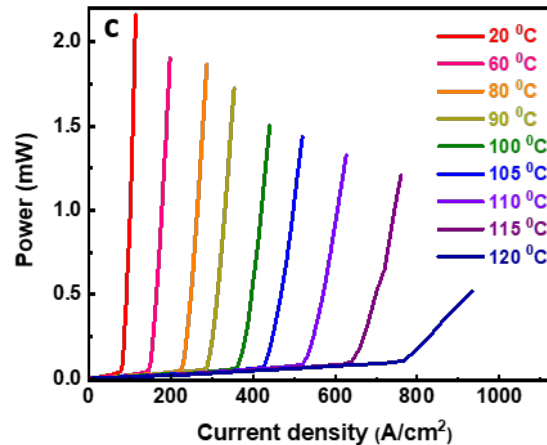
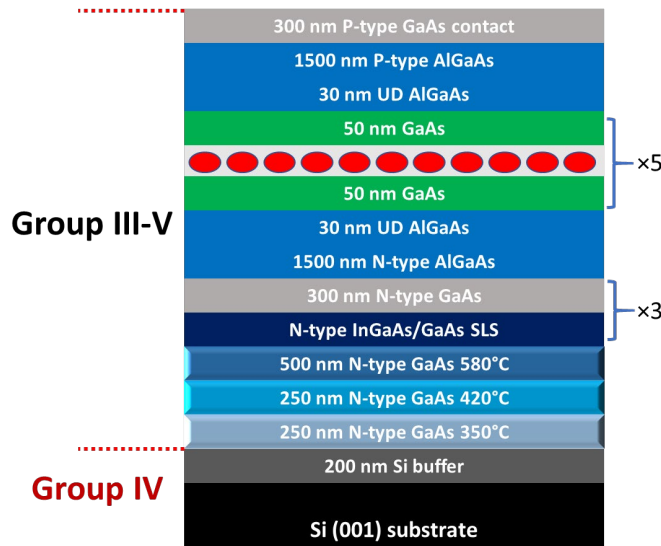
- On-axis Si (001) substrate
- CW operation
- $J_{th} = 160 \text{ A/cm}^2$ (RT)
- $\lambda_{peak} = 1330 \text{ nm}$
- $T_{max} = 52 \text{ }^\circ\text{C}$ (CW); 102 (Pulsed)

S. Chen et al., **Optics Express** 25, 4632 (2017)

K. Li et al., **J CRYST GROWTH** 511, 56 (2019)

All MBE grown QD laser on on-axis Si (001)

2020



Key Features

- All MBE grown
- Si (001) substrates used were not intentionally selected before epitaxy
- Special twin MBE system combining Group-IV and III–V growth chamber

- On-axis Si (001) substrate
- Pulsed operation
- $J_{th} = 83.3$ A/cm² (RT)
- $\lambda_{peak} = 1310$ nm
- $T_{max} = 120$ (Pulsed)

Outline

- I. Why monolithic III-V lasers on Si
- II. Quantum dots for monolithic III-V/Si integration
- III. Review of monolithic III-V quantum dot lasers on Si
- IV. Conclusion**

Conclusion

- Significance of epitaxial growth of III-V lasers on silicon has been introduced
- Challenges for epitaxial growth of III-V materials on silicon have been discussed
- Unique advantages of using quantum dots as the active for monolithic III-V/Si integration have been discussed
- Recent progress in monolithic 1.3 μm InAs quantum dot lasers on silicon achieved by UCL has been reviewed

Acknowledgement

❖ UCL colleagues

Mingchu Tang, Alwyn Seeds and Huiyun Liu

❖ Partners



香港中文大學(深圳)
The Chinese University of Hong Kong, Shenzhen

❖ Funding



❖ PhD student

Mengya Liao, Ying Lu, Shujie Pan, Victoria Cao

Thanks!

siming.chen@ucl.ac.uk